



JPW/AF/181
PATENT

Docket No.: ACT-367

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2815

Examiner: Landau, Matthew C.

Serial No.: 10/619,172

Filed: July 11, 2003

In re Application of: John McCollum, et al.

For: LOW-CAPACITANCE INPUT/OUTPUT AND ELECTROSTATIC
DISCHARGE CIRCUIT FOR PROTECTING AN INTEGRATED CIRCUIT
FROM ELECTROSTATIC DISCHARGE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 5, 2005.

Signed

Carrie Chandler
Carrie Chandler

TRANSMITTAL LETTER

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find a Supplemental Response to the Final Office Action dated June 6, 2005 and the Advisory Action dated August 18, 2005, and a check in the amount of \$120.00 for one (1) month extension of time in connection with the above-identified application.

In the event the Patent Office charges a fee for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 50-0612. An additional copy of this page is enclosed.

Dated: October 5, 2005

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Respectfully submitted,
SIERRA PATENT GROUP, LTD.

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